

# 湖南飞优特电子科技有限公司

# **Hunan Future Electronics Technology Co. Ltd**

# SPECIFICATION FOR LCD MODULE

Customer	:			
Product Mode	I:FUT04	130WQ	07Q-LC	M-AO
Sample code:				
Designed by	Chec	ked by		Approved by
Final Approv	al by Cust	omer		
LCM Mach	ninery OK		LCM OK	
Checked By			NG, Pro	oblem survey:
LCM Disp	,			
Checked By		Approve	ed By	

\*\* The specification of "TBD" should refer to the measured value of sample. If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.

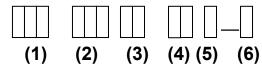
# **Revision History**

Version	Contents	Date	Note
Α	Original	2021-4-17	
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### **Contents**

No.	Item	Page
1	Numbering System	4
2	Scope	5
3	Normative Reference	5
4	Definitions	5
5	Technology Specifications	7
6	Inspection Standards	17
7	Reliability Test	22
8	Handling Precautions	22
9	Precaution for use	24
10	Package Drawing	25
11	Outline Dimension	26

# 1. Numbering System



No	Definition	Specifications
(1)	TFT LCM Productor No.	FUT Hunan Future Electronics Technology Co. Ltd
(2)	Display monitor opposite angle line size	Unit : inch
(3)	Product Resolution	QQ: QQVGA 128*160 CC: QCIF 186*220 QV: QVGA 240*320, HV: HVGA320*480, WV:WVGA 480*800, QH:QHD 540*960 HD:720*1280, FHD1080*1920
(4)	Product Development Series No.	By two figures characters expression from 01 to 99
(5)	LCD Type	AAUO; MCMI; CCPT; BBOE; GLG; SCTC; HHSD; TTianma; YHydis; IINNOLUX; LIVO
(6)	Productor Development edition No.	By The English litters : A~ Z

#### 2. Scope

This specification applies to the TFT LCD module which is designed and manufactured by LCM Factory of Hunan Future Electronics Technology Co. Ltd

#### 3. Normative Reference

GB/T4619-1996 《 Liquid Crystal Display Test Method》

GB/T2424 《 Basic environmental Testing Procedures for Electric and Electronic Products.》

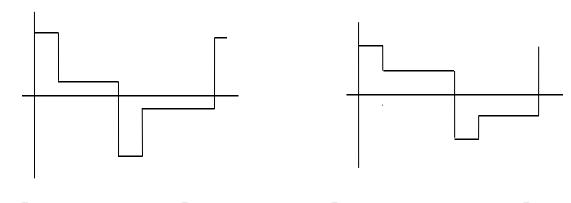
GB/T2423 《Basic Testing Procedures for Electric and Electronic Products》

IEC61747-1 《SIXTH PARTGB2828`2829-87《National Standard of PRC》

#### 4. Definitions

#### 4.1 Definitions of Vop

The definitions of threshold voltage Vth1, Vth2 the following typical waveforms are applied on liquid crystal by the method of equalized voltage for each duty and bias.



Selected waveform
I

I non-selected waveform ]

① Vth1: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of selected waveform

② Vth2: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of non-selected waveform

(f<sub>f</sub>=80Hz, 
$$\Phi$$
=10°  $\theta$  =270° at 25°€)

③ Vop: (Vth1(50%)+Vth2(50%))/2 ( $f_f=80$ Hz,  $\Phi=10^{\circ}$   $\theta=270^{\circ}$  at 25°C)

#### 4.2 Definition of Response Time Tr, Td

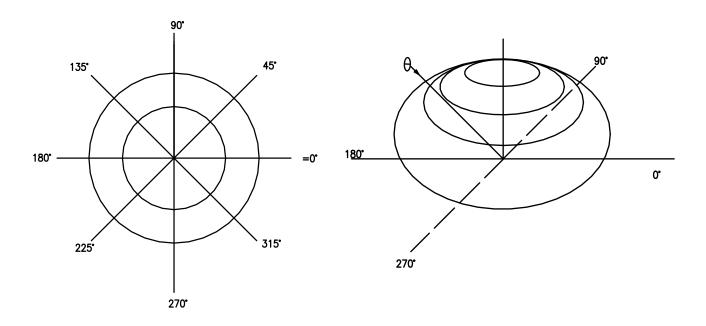
- ①Tr: The time required which the brightness of segment becomes 10% from 100% when waveform is switched to selected one from non-selected one. (f<sub>f</sub>=80Hz, Φ=10°θ=270°at 25°C)
- ②Td: The time required which the brightness of segment becomes 90% from 10% when waveform is switched to selected one from selected one. ( $f_f$ =80Hz, Φ=10°θ=270°at 25°C)

#### 4.3 Definition of Contrast Ratio Cr

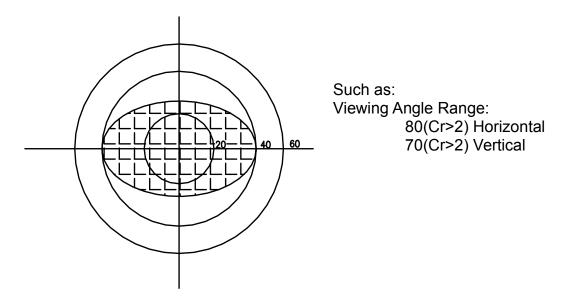
Cr=A/B

- ① A: Segments brightness in case of non-selected waveform
- ② B: Segments brightness in case of selected waveform

#### 4.4 Definition of Angle and Viewing Range



Angular Graph: Constrast Ratio



### 5. Technology Specifications

#### 5.1 Features

The LCD adopts one backlight with High brightness 7-lamps white LED.

Construction: .4.3" a-Si color TFT-LCD ,With INX Cell, White LED backlight and FPC .

5.2 General Specifications

No.	Item	Specification
1	LCD size	4.3 inch
2	Resolution	480(RGB)X272
3	Display mode	6 o' clock
4	Pixel pitch	66(H) X 198(V) um
5	Active area	95.04(H) X 53.856(V)
6	Module size	105.5(H)X67.2(V)X2.9(D)mm
7	Touch Panel	without
8	Pixel arrangement	RGB-stripe
9	Interface	SPI+RGB 24bit

#### **5.3 Interface Pin Connection**

Pin No	Symbol	Description
1	VLED-	LED back light(Cathode)
2	VLED+	LED back light(Anode)
3	GND	Ground
4	VDD	Power supply
5-12	R0-R7	RED Data bus
13-20	G0-G7	GREEN Data bus
21-28	B0-B7	BLUE Data bus
29	GND	Ground
30	PCLK	Dot clock signal for RGB interface operation.
31	DISP	Display on/off
32	HSYNC	Horizontal sync. signal in DPI interface mode
33	VSYNC	Frame synchronizing signal for RGB interface operation.
34	DE	Data ENABLE signal for RGB I/F mode
35	NC	No Connected
36	GND	Ground
37	XR (NC)	Touch panel X-Right
38	YD (NC)	Touch panel Y-Down
39	XL (NC)	Touch panel X-Left
40	YU (NC)	Touch panel Y-Up

5.4 Absolute Max. Rating

Itom	Cumbal	Values		Unit	
Item	Symbol	Min.	Max.	Offic	
	VDDI	-0.3	4.6	V	
Power Voltage	VDD	-0.3	4.6	V	
	VIN	-0.3	IOVCC+0.3	V	
Backlight forward current	ILED	0	25	mA (For each LED)	
Operation Temperature	T <sub>OP</sub>	-20	70	${\mathbb C}$	
Storage Temperature	T <sub>ST</sub>	-30	80	${\mathbb C}$	

#### 5.5 DC Characteristics

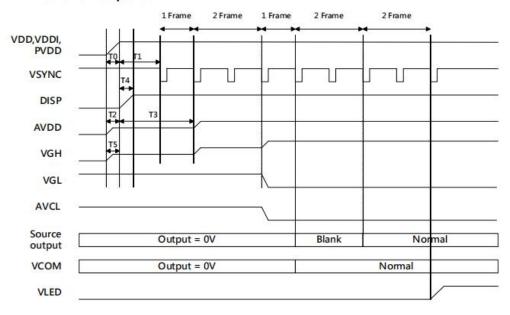
Item	Symbol		Values		Remark	
цеш	Symbol	Min.	Тур.	Max.	Offic	Remark
Logic Supply Voltage	IOVCC	1.65	1.8/2.8	3.3	V	-
Analog Supply Voltage	Vdd	3.0	3.3	3.6	V	-
VGL Voltage	VGL	-10.5	-10	-9.5	V	-
VGH Voltage	VGH	14.5	15	15.5	V	-
Input High Voltage	Vih	0.7vddi	-	VDDI	V	Digital input pins
Input Low Voltage	VIL	GND	ı	0.3VDDI	mΑ	Digital input pins
Output High Voltage	VoH	VDDI-0.4	-	VDDI	mA	Digital input pins
Output Low Voltage	VoL	GND	ı	GND+0.4	W	Digital input pins
(Panel+LSI)	Black Mode	-	1	90	mA	VDD=3.3V
Power Consumption	Sleeping Mode	-	-	150	uA	VDD=3.3V

### 5.6 LED Back Light Specification (7 White Chips)

Item	Symbol	Condition	Min	Тур	Max	Unit
Forward Voltage	Vf	lf=20mA	-	22.4	-	V
Uniformity (with L/G)	ΔBp	lf=20mA	75	80	-	%
Luminance for LCM	/	lf=20mA	-	TBD	-	cd/m <sup>2</sup>
Backlight Power Consumption	WBL	lf=20mA	-	448	-	mW
Backlight Color	White					

#### 5.7 Power Sequence

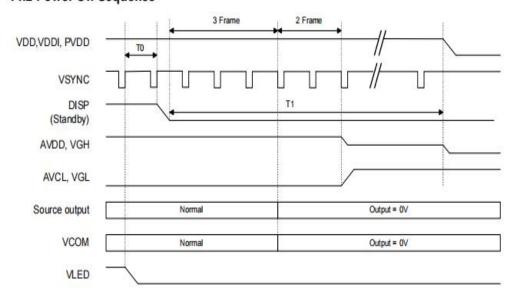
#### 14.1 Power On Sequence



Item	Description	Min. Time
ТО	Determined by the external power	
T1	Time from stable VDD, VDDI, PVDD set-up to the first VSYNC	T1=0
T2	Time from AVDD=0V to AVDD=3.3V	T2=T0
T3	Time from AVDD=3.3V to AVDD=6.0V	T3=T1+ (1*Frame)
T4	Time from stable VDD, VDDI, PVDD set-up to DISP asserted	T4=0
T5	Time from VGH=0V to VGH=3.3V	T5=T0

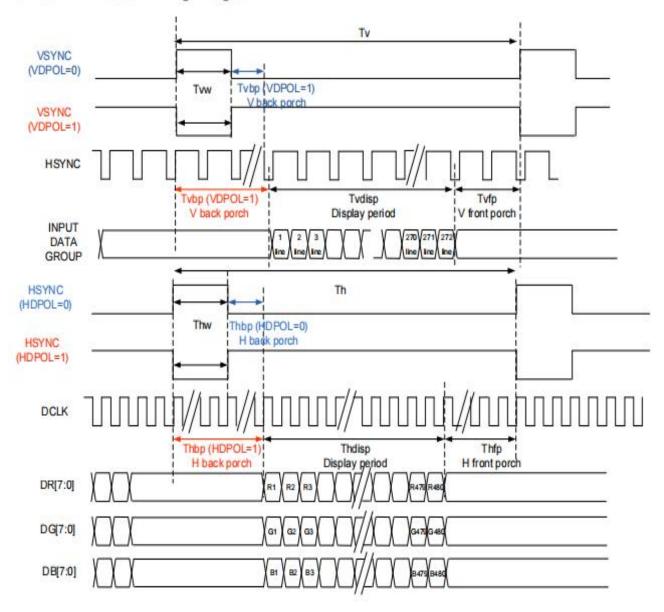
Note: Recommend the LCM power on rise time T0= 0~ 1ms.

#### 14.2 Power Off Sequence

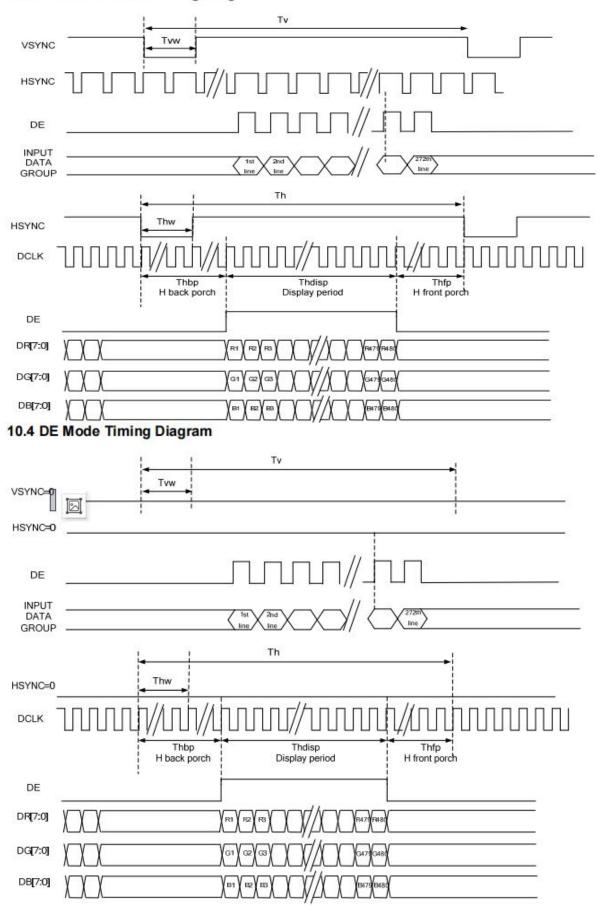


Item	Description	Min. Time
T0	Time from backlight power off to DISP="L"	1*Frame
T1	Time from DISP="L" to LCM Power off	5*Frame

# 5.8 Timing Conditions AC Characteristics 10.2 SYNC Mode Timing Diagram



#### 10.3 SYNC-DE Mode Timing Diagram

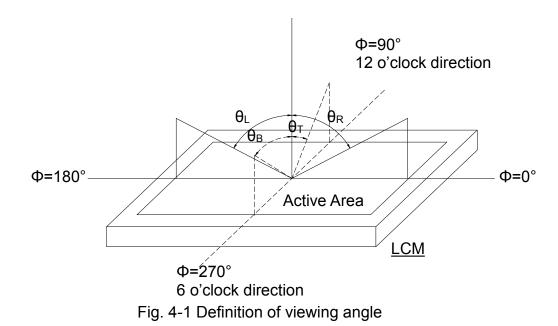


### 5.9 Optical specifications

Itama	Itom		Conditions	Specifications			Limit
Item		Symbol	Conditions	Min.	Тур.	Max.	Unit
Transmitta (w/o APC		Т%	V.C.		5.84	7.	%
Contrast R	tatio	CR	Viewing normal angle $\theta_X = \theta_Y = 0$	(4)	500	-	-
Response	Time	T <sub>on+</sub> T <sub>off</sub>	0	1775	25	50	ms
	11	θ <sub>X+</sub>			70	×	
Viewing Angle	Hor.	θ <sub>X</sub> .	Center		70	E	deg.
Viewing Angle	Ver.	$\theta_{Y+}$	CR>10		50	7:	
	vei.	θ <sub>Y</sub> .			60	-	
	Red	Rx		0.597	0.627	0.657	=
	Red	Ry		0.311	0.341	0.371	70
	Green	Gx		0.278	0.307	0.337	2
CF only Color	Green	Gy	Viewing	0.509	0.539	0.569	¥
Chromaticity	Divis	Bx	normal angle	0.108	0.138	0.168	-
(CIE 1931)	Blue	Ву	$\theta_X = \theta_Y = 0$	0.066	0.096	0.126	751
	White	Wx		0.285	0.315	0.345	2:
	vvnite	Wy		0.296	0.326	0.356	
	Color	Gamut	]	-	55.4	-	%

Note 1: Definition of viewing angle range

Normal line  $\theta = \Phi = 0^{\circ}$ 



Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Viewing angle is measured by ELDIM-EZ contrast/Height :1.2mm ,Response time is measured by Photo detector TOPCON BM-5A, other items are measured by BM-7A/Field of view: 1° /Height: 500mm.)

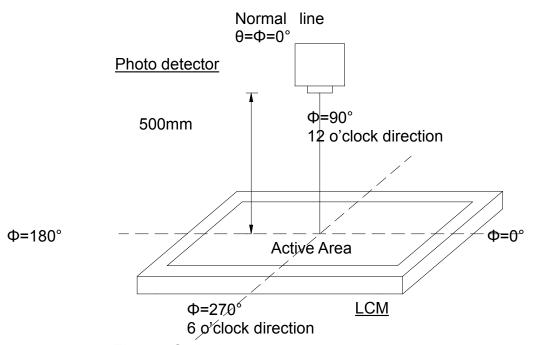


Fig. 4-2 Optical measurement system setup

#### Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time  $(T_{ON})$  is the time between photo detector output intensity changed from 90% to 10%. And fall time  $(T_{OFF})$  is the time between photo detector output intensity changed from 10% to 90%.

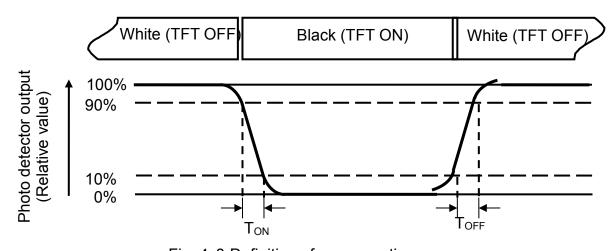


Fig. 4-3 Definition of response time

#### Note 4: Definition of contrast ratio

Contrast ratio (CR) =  $\frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$ 

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is I<sub>LED</sub>=60mA.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4 ). Every measuring point is placed at the center of each measuring area.

Inter of each measuring area.

Luminance Uniformity (Yu) = 
$$\frac{B_{min}}{B_{max}}$$

L-----Active area length W----- Active area width

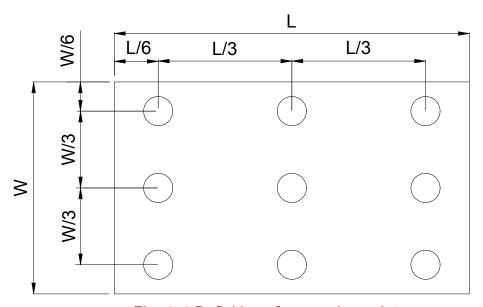


Fig. 4-4 Definition of measuring points

 $B_{\text{\scriptsize max}}\!\!:$  The measured maximum luminance of all measurement position.

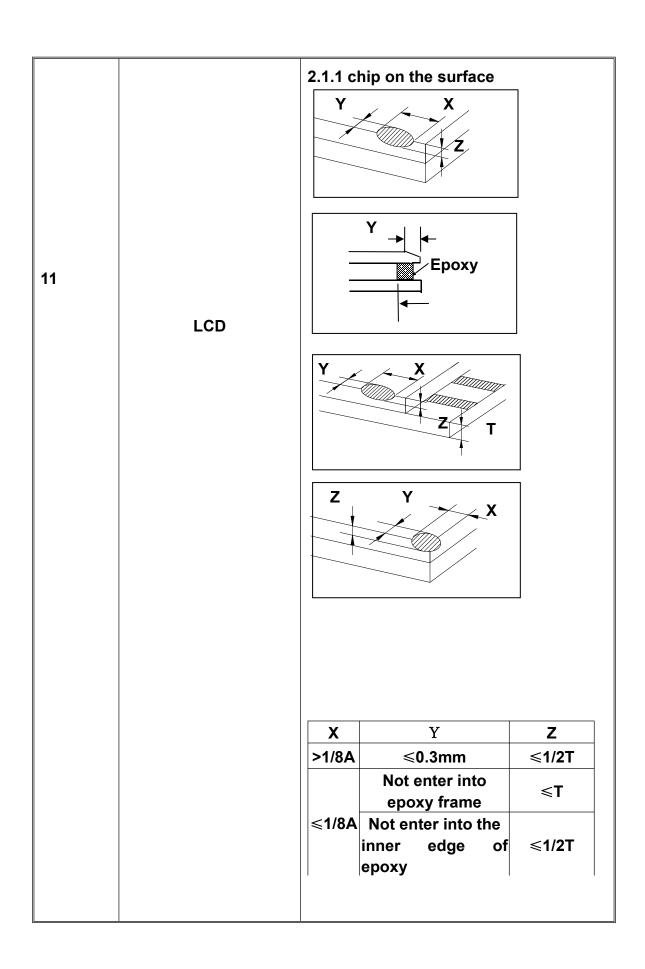
B<sub>min</sub>: The measured minimum luminance of all measurement position.

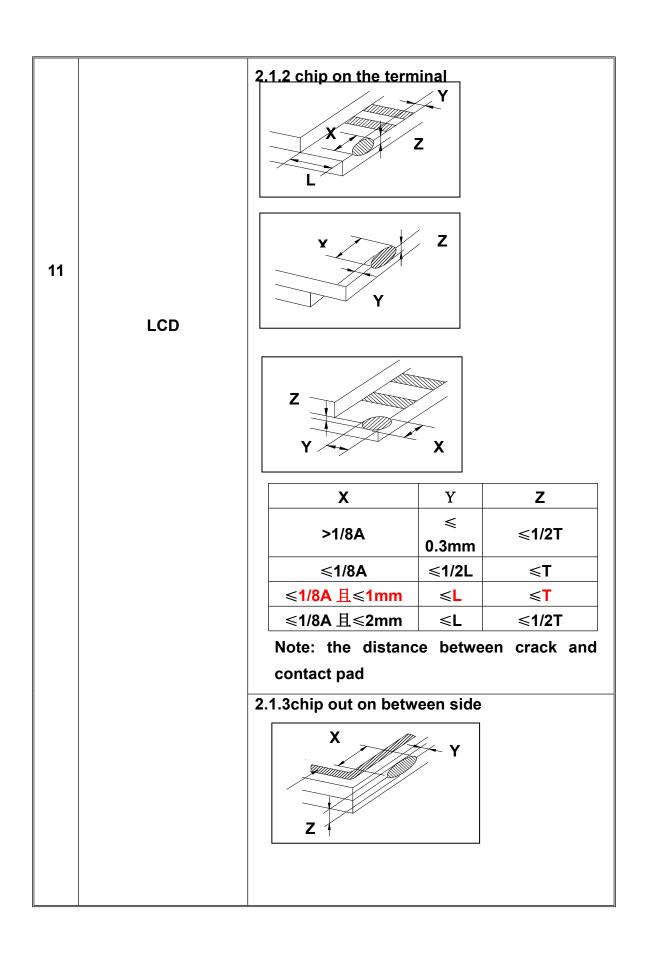
Note 8: Testing equipment BM-7

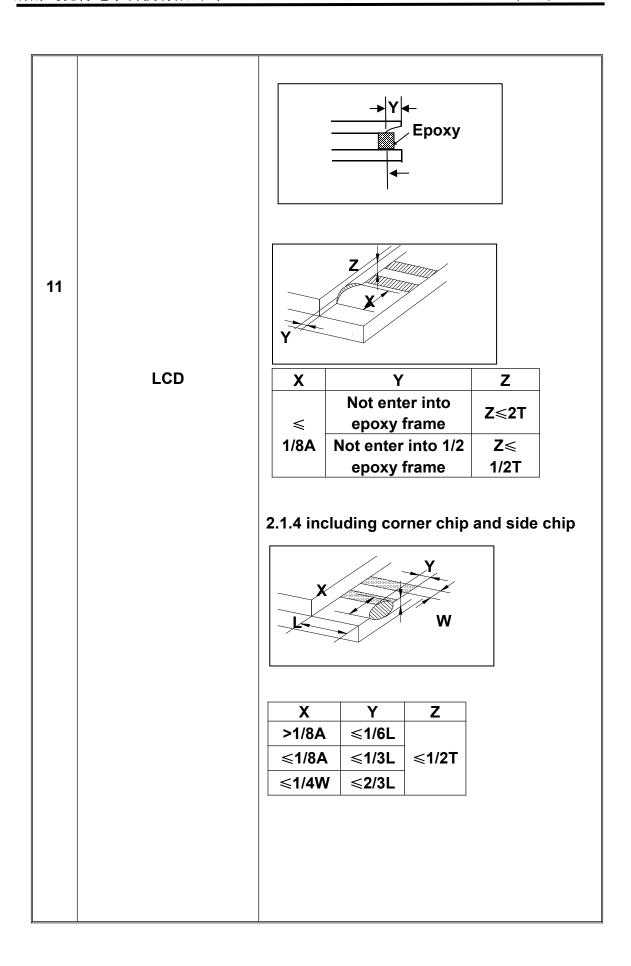
# **6. Inspection Standards**

No.	Item	Criterion				
1	Short or open circuit	Not allow				
	LC leakage					
	Flickering					
	No display					
	Wrong viewing direction					
	Wrong Back-light					
2	Contrast defect	Refer to approval sample				
	Background color deviation					
3	Point defect, Black/White /Died spot, dust,eyewinker (incl. Polarizer)	Y		Point	Acceptable Qty.	
		<b>X</b>		Size	_	
				φ≤0.10	Disregard	
			-	0.10<φ≤0.15	+	
	$\phi = (X+Y)/2$			0.15<φ≤0.20		
				φ>0.20	0	
					Unit: mm	
4	Line defect	<u></u>				
		<u> </u>		Line	Acceptable Qty.	
		<u></u>	L	W	Diagrand	
		L		0.015 ≥ W	Disregard	
			2.0≥	L 0.025≥ W	2	
		_	1.0≥	L 0.05≥ W	1	
					Unit: mm	
5	Rainbow	Not more that viewing area.	n two	color cha	nges across the	

No.	Item	Criterion			
6	Segment pattern W = Segment width φ = (X+Y)/2	(1) Pin hole $\phi < 0.10 \text{mm is acceptable}.$ $Y$			
7	Back-light	<ul><li>(1) The color of backlight should correspond its specification.</li><li>(2) Not allow flickering</li></ul>			
8	Soldering	(1) Not allow heavy dirty and solder ball on PCB.  (The size of dirty refer to point and dust defect)  (2) Over 50% of lead should be soldered on Land.			
9	Wire	<ul><li>(1) Copper wire should not be rusted</li><li>(2) Not allow crack on copper wire connection.</li><li>(3) Not allow reversing the position of the flat cable.</li><li>(4) Not allow exposed copper wire inside the flat cable.</li></ul>			
10	PCB	(1) Not allow screw rust or damage. (2) Not allow missing or wrong putting of component.			







# 7. Reliability Test Conditions And Methods

Item	Test Conditions		Remark
High Temperature Storage	Ta = 80°C	48hrs	
Low Temperature Storage	Ta =-30°C	48hrs	
High Temperature Operation	Ts = 70°C	48hrs	
Low Temperature Operation	Ta = -20°C	48hrs	
Operate at High Temperature and Humidity	60℃, 90%RH max.	48hrs	Operation
Thermal Shock	-20°C~ +70°C 10 cycles 1Hrs/cycle		Non-operation
Vibration Test	Frequency:10Hz~55Hz~10Hz Amplitude:1.5mm X,Y,Z direction for total 3hours (Packing Condition)		
Drooping Test	Drop to the ground from 1M height one time every side of carton. (Packing Condition)		
Electrostatic Discharge	Contact=±4KV, class B Air=±8KV, class B		

#### 8. Handling Precautions

#### 8.1 Mounting method

The LCD panel of FEXDA LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

#### 8.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent [recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl) , Salfur (S)

If goods were sent without being sili8con coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Salfur (S) from customer, Responsibility is on customer.

#### 8.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

#### 8.4 packing

- Module employ LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

#### 8.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

#### 8.6 storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
   [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

#### 8.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

#### 9. Precaution for use

#### 9 1

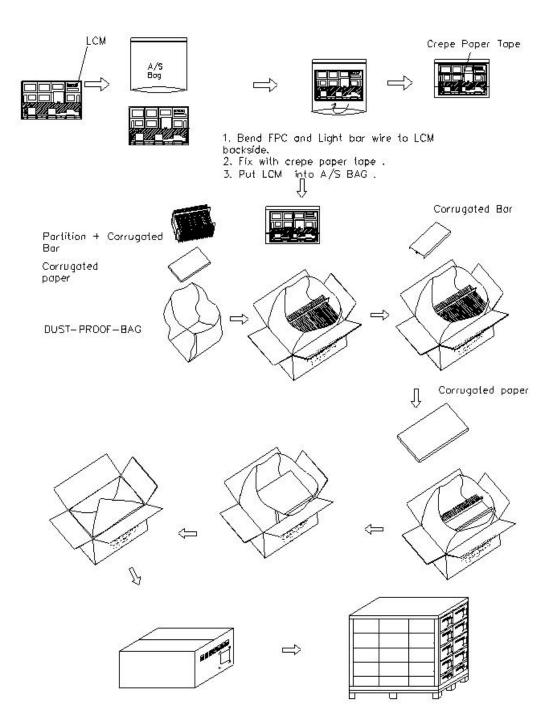
A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

#### 9.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to FEXDA, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

### 10. Package Drawing



#### 11. Outline Dimension

