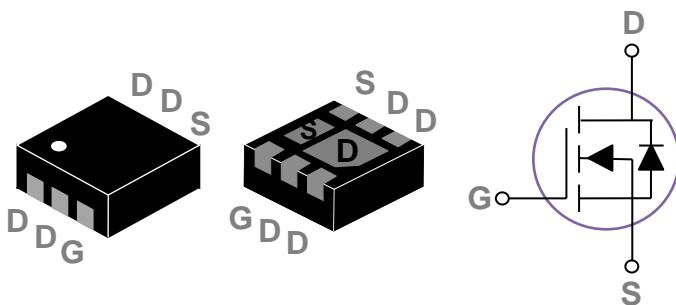


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### DFN2x2-6L 2EP Pin Configuration



BVDSS	RDS(ON)	ID
65V	17.4mΩ	7A

### Features

- 65V, 7A,  $RDS(ON) = 17.4\text{m}\Omega$  @  $VGS = 10\text{V}$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	65	V
$V_{GS}$	Gate-Source Voltage	+20/-12	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ\text{C}$ )	7	A
	Drain Current – Continuous ( $T_A=70^\circ\text{C}$ )	5.6	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	28	A
$P_D$	Power Dissipation ( $T_A=25^\circ\text{C}$ )	2	W
	Power Dissipation – Derate above 25°C	0.016	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	65	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=60\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=48\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=85\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=20\text{V}$ , $V_{DS}=0\text{V}$	---	---	100	$\text{nA}$

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{GS}=10\text{V}$ , $I_D=2\text{A}$	---	14.5	17.4	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=1.5\text{A}$	---	24	31	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	1.2	1.6	2.5	V
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=1\text{A}$	---	3	---	S

**Dynamic and switching Characteristics**

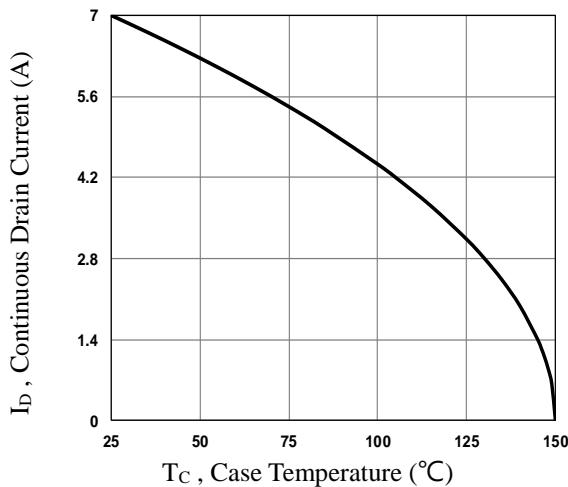
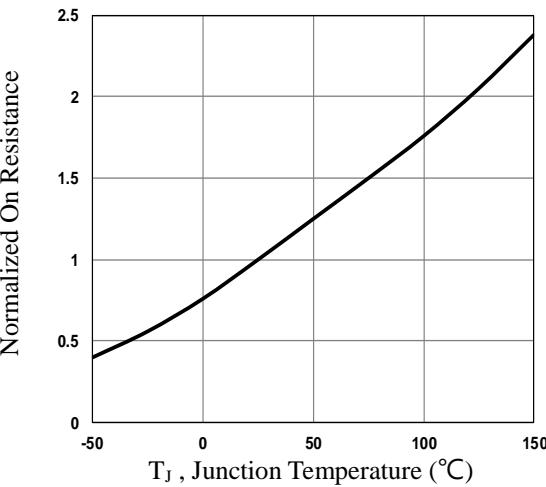
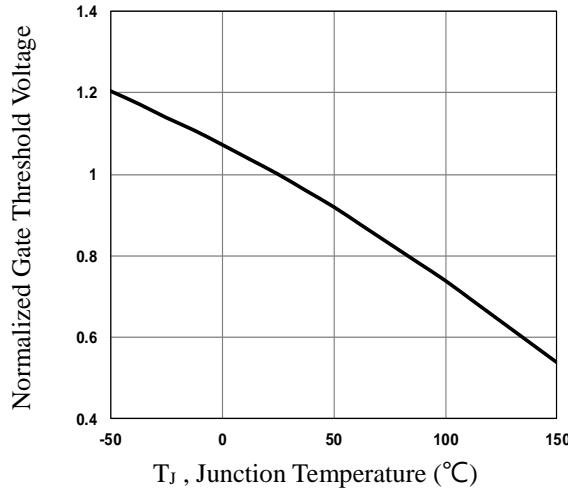
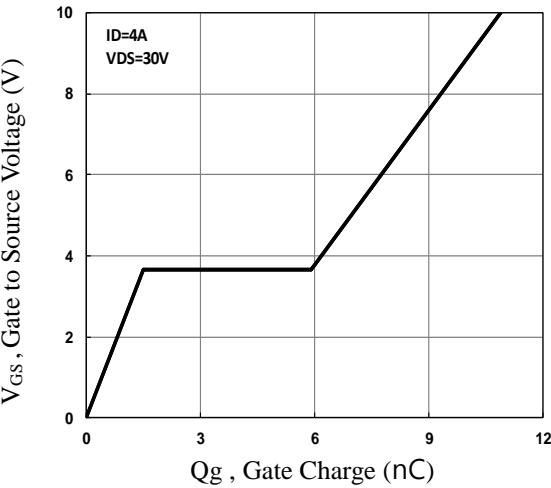
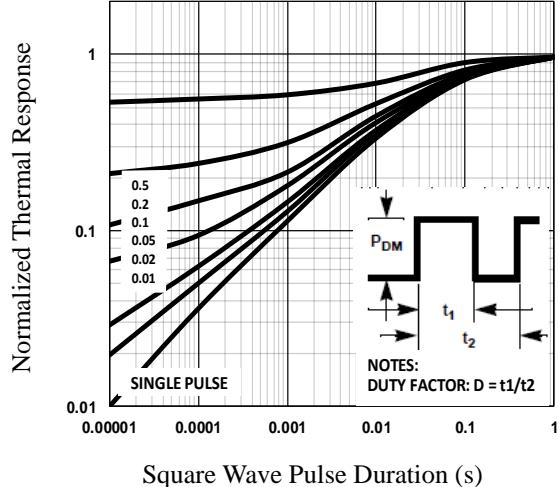
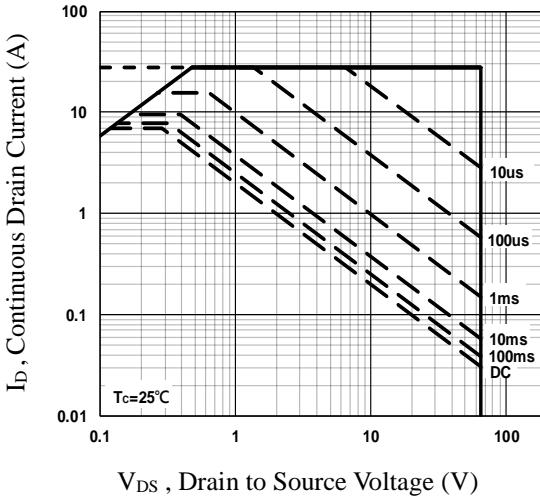
$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{DS}=30\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=4\text{A}$	---	10.9	22	nC
$Q_{gs}$	Gate-Source Charge <sup>2, 3</sup>		---	1.5	3	
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>		---	4.4	9	
$T_{d(on)}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{DD}=30\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=6\Omega$ $I_D=4\text{A}$	---	8	16	ns
$T_r$	Rise Time <sup>2, 3</sup>		---	12	24	
$T_{d(off)}$	Turn-Off Delay Time <sup>2, 3</sup>		---	25	50	
$T_f$	Fall Time <sup>2, 3</sup>		---	18	36	
$C_{iss}$	Input Capacitance	$V_{DS}=30\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	653	1300	pF
$C_{oss}$	Output Capacitance		---	192	380	
$C_{rss}$	Reverse Transfer Capacitance		---	27	60	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $f=1\text{MHz}$	---	0.8	---	$\Omega$

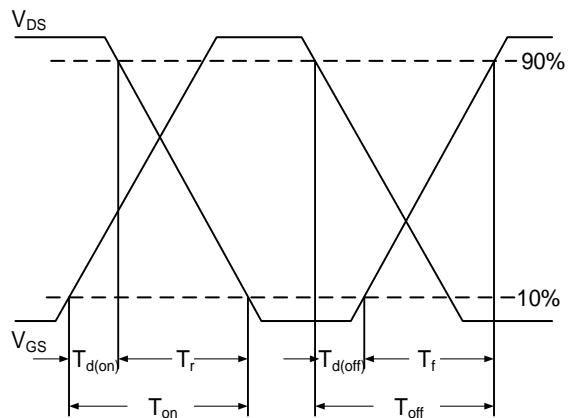
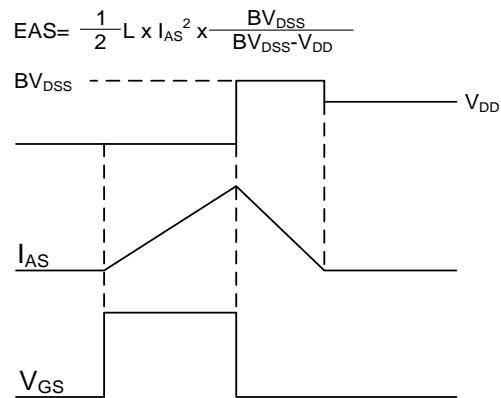
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	7	A
			---	---	14	A
$V_{SD}$	Diode Forward Voltage <sup>3</sup>	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V
$t_{rr}$	Reverse Recovery Time	$V_R=50\text{V}$ , $I_s=4\text{A}$ $di/dt=100\text{A}/\mu\text{s}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	30	---	ns
			---	15	---	nC

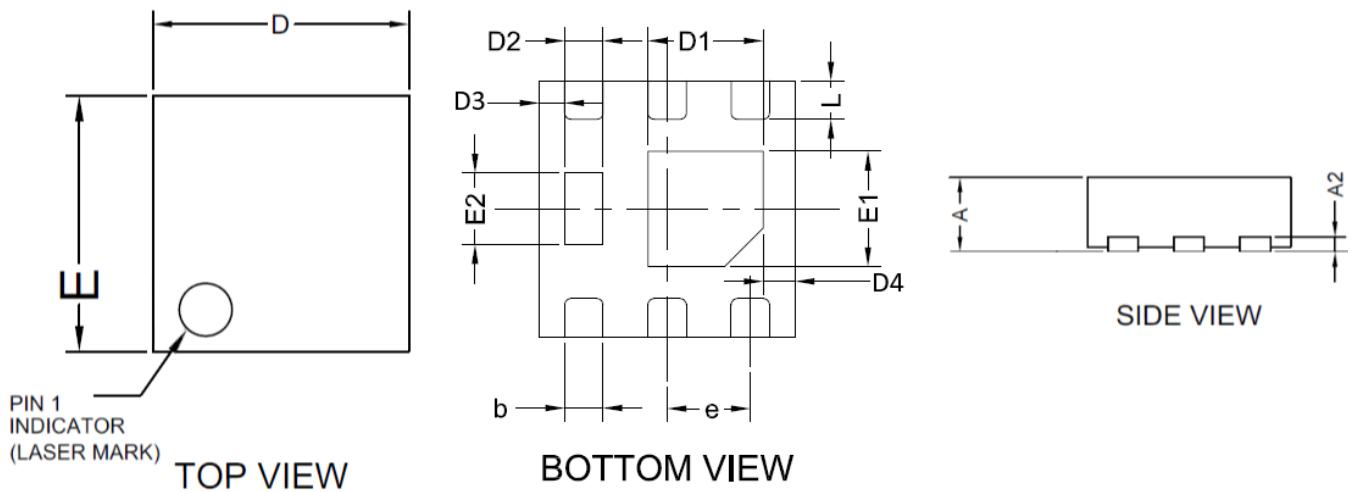
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs.  $T_c$** 

**Fig.2 Normalized RD<sub>SON</sub> vs.  $T_j$** 

**Fig.3 Normalized V<sub>th</sub> vs.  $T_j$** 

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Impedance**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 EAS Waveform**

## DFN2x2-6L 2EP PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
<b>A</b>	<b>0.800</b>	<b>0.500</b>	<b>0.031</b>	<b>0.019</b>
<b>A2</b>	<b>0.250</b>	<b>0.145</b>	<b>0.010</b>	<b>0.006</b>
<b>b</b>	<b>0.350</b>	<b>0.250</b>	<b>0.014</b>	<b>0.010</b>
<b>D</b>	<b>2.100</b>	<b>1.900</b>	<b>0.083</b>	<b>0.075</b>
<b>D1</b>	<b>1.000</b>	<b>0.800</b>	<b>0.040</b>	<b>0.031</b>
<b>D2</b>	<b>0.350</b>	<b>0.250</b>	<b>0.014</b>	<b>0.010</b>
<b>D3</b>	<b>0.200BSC</b>		<b>0.008BSC</b>	
<b>D4</b>	<b>0.200BSC</b>		<b>0.008BSC</b>	
<b>E</b>	<b>2.100</b>	<b>1.900</b>	<b>0.083</b>	<b>0.075</b>
<b>E1</b>	<b>1.050</b>	<b>0.800</b>	<b>0.041</b>	<b>0.031</b>
<b>E2</b>	<b>0.66</b>	<b>0.46</b>	<b>0.026</b>	<b>0.018</b>
<b>e</b>	<b>0.650BSC</b>		<b>0.026BSC</b>	
<b>L</b>	<b>0.350</b>	<b>0.250</b>	<b>0.014</b>	<b>0.010</b>

## DFN2x2-6L 2EP RECOMMENDED LAND PATTERN

